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PATENT & TRADEMARK OFFICE

FORM PTO-1449	SERIAL NO.	ATTORNEY DOCKET NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	10/680,051	JP920020067US1
	FILING DATE: October 7, 2003	GROUP ART UNIT: 2652
(use several sheets if necessary)	APPLICANT(S): Hideo Asano et al.	2824

REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION	
						YES	NO

NON-PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT (Including Author, Title, Source, and Pertinent Pages
HN	A1	R. Scheuerlein, W. Gallagher, S. Parkin, A. Lee, S, Ray, R Robertazzi, W. Reohr, "A 10ns Read and Write Non-Volatile Memory Array Using A Magnetic Tunnel Junction and FET Switch in each Cell," IEEE ISSCC Dig. Tech. Papers, 2000, pp. 128-129.
HN	A2	M. Durlam, P. Naji, M. DeHerrera, S, Tehrani, G. Kerszykowski, K, Kyler, "Nonvolatile RAM base on Magnetic Tunnel Junction Elements" , IEEE ISSCC Dig. Tech. Papers, 2000 pp 130-131.
HN	A3	Hurd, Kevin A., "A 600MHz 64b PA-RISC Microprocessor", IEEE ISSCC Dig. Tech. Papers, 2000 pp 94-95.

EXAMINER HIEN NGUYEN	DATE CONSIDERED 5/12/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

FORM PTO-1449 **LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT** (use several sheets if necessary)	SERIAL NO. Not yet assigned	ATTORNEY DOCKET NO. JP920020067US1
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REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
HN	A1	6,111,783	08/29/2000	Tran et al.	365/171	06/16/1999

FOREIGN PATENT DOCUMENTS

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						YES	NO

NON-PATENT DOCUMENTS

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EXAMINER HIEN NGUYEN	**DATE CONSIDERED** 5/12/05

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